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   Wanner, W.; Lewis, P.; Roujean, J.-L.;
   Geoscience and Remote Sensing Symposium, 1996. IGARSS '96. 'Remote Sensing for a Sustainable Future.', International
   Volume 3, 27-31 May 1996 Page(s):1408 - 1410 vol.3
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Yen-Shin Lai; Hsien-Chung Huang; Yung-Shun Kuan; Chung-Ming Young;
Applied Power Electronics Conference and Exposition, 1998. APEC '98. Conference Proceedings 1998.
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☐ 2. **Optimisation of microprocessor-based random PWM schemes for power inverters with low switching frequencies**
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Industry Applications, IEEE Transactions on
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☐ 4. **Failure coverage of functional test methods: a comparative experimental evaluation**
Velazco, R.; Bellon, C.; Martinet, B.;
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Digital Object Identifier 10.1109/TEST.1990.114124
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5. **Dream chip 1: a timed priority queue**
Kahrs, M.;
Micro, IEEE
Volume 13, Issue 4, Aug. 1993 Page(s):49 - 51
Digital Object Identifier 10.1109/40.229715

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6. **Speed versus Accuracy Trade-Offs in Microarchitectural Simulations**
Yi, J.J.; Sendag, R.; Lilja, D.J.; Hawkins, D.M.;
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Volume 56, Issue 11, Nov. 2007 Page(s):1549 - 1563
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7. **The SYNARGOS ambient noise measurement buoy**
Burke, S.; Buck, B.; Luther, C.;
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Volume 14, Sep 1982 Page(s):626 - 631

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8. **DPA Using Phase-Based Waveform Matching against Random-Delay Countermeasure**
Nagashima, S.; Homma, N.; Imai, Y.; Aoki, T.; Satoh, A.;
Circuits and Systems, 2007. ISCAS 2007. IEEE International Symposium on
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9. **Experiences in dynamic placement of actors on multicomputer systems**
Di Santo, M.; Frattolillo, F.; Iannello, G.;
Parallel and Distributed Processing, 1995. Proceedings. Euromicro Workshop on
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Digital Object Identifier 10.1109/EMPDP.1995.389146

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10. **Hardware implementation for fast convolution with a PN code using field programmable gate array**
Alaqeeli, A.; Starzyk, J.;
System Theory, 2001. Proceedings of the 33rd Southeastern Symposium on
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Digital Object Identifier 10.1109/SSST.2001.918517

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11. **Comparison and Impact of Substrate Noise Generated by Clocked and Clockless Digital Circuitry**
Le, J.; Hanken, C.; Held, M.; Hagedorn, M.; Mayaram, K.; Fiez, T.S.;
Conference 2006. IEEE Custom Integrated Circuits
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